#### 1501324 Sequence Control and PLC

Course Description:

Basic sequence control; Programmable Logic Controllers (PLCs); Programming of PLC systems; Ladder program development; Sequential control facilities; Advanced features of PLCs; PLC Communications and automation\*; Plant emulation; Process monitoring and control\*.

Learning outcome:

- 1. Students are able to implement PLC systems.
- 2. Students are able to simulate PLC systems.
- 3. Students are able to work on PLC applications.

Lecturer:

Assoc. Prof. Punnarumol Temdee, Ph.D. Asst. Prof. Roungsan Chaisricharoen, Ph.D. Asst. Prof. Santichai Wicha, Ph.D. Lect. Chayapol Kamyod, Ph.D.

Credit: 3(2-2)

Lecture: 30 Hours (6 hours of modified content) Lab: 30 Hours (6 hours of modified content)

Assessments:

Attendance	10%
HW/CW	20%
Midterm	25%
Final	25%
Project	20%

#### Lecture (seminar):

Content	Hours
PLC HW and SW	4
Paradigm of PLC logics	4
PLC programming	4
PLC communications	4
Ladder programs	4
Advanced PLC	4
Simulation and emulation*	2
Process monitoring*	2
Process control*	2

(\*modified in the framework of an Erasmus + project: Asean Factori 4.0 Across South East Asian Nations: From Automation and Control Training to the Overall Roll-out of Industry 4.0 609854-EPP-1-2019-1-FR-EPPKA2-CBHE-JP)

#### Lab (internship):

Content	Hours
PLC configuration	4
PLC wiring	4
PLC digital I/O	4
PLC analog I/O	4
PLC simulation	4
PLC communications	4
Process monitoring via HMI*	2
Process control via HMI*	2
Emulation card*	2

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Program: Bachelor program in Computer EngineeringCredit: 3(2-2)Lecture: 30 HoursLab: 30 Hours



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609854-EPP-1-2019-1-FR-EPPKA2-CBHE-JP

2<sup>nd</sup> Semester, Academic Year: 2023

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# Lecture 01: Simulation and emulation



## HARDWARE IN THE LOOP SYSTEM

#### Home made electronic interface card



#### 24 sensors and 24 actuators

- 16 digital inputs / 16 digital outputs
- 8 analog inputs / 8 analog outputs
- Less than 500€
- Reasonable timing performance ( < 10 ms response time)</p>
- Easily chain (Ethernet addressing)





## **TWO MAIN APPLICATION FIELDS**

### Industrial automation: PLC, SCADA et OPC





# Smart-grid communication IEC 61850



## A SIMPLE APPLICATION EXAMPLE (~70 I/O)

Tennessee Eastman Chemical Company (O. Koucham PhD benchmark)





## Supervision Manufacturing Message Specification – MMS TCP/IP Unicast Generic Object Oriented Substation Event – GOOSE Ethernet Multicast Bay network Protection relays Process Bus Sample Values – SV – Ethernet Multicast -> HSR/PRP Stand Alone Measurement Units

## **61850 SMART GRID APPLICATIONS**



## **61850 SMART GRID APPLICATIONS**



- Cybersecurity applications (M. Kabir-Querrec PhD benchmark)
  - False event injection (GOOSE attack)
  - False measures injection (SMV attack)
  - Resilient architectures
    - C&ESAR 2015



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### **INDUSTRIAL PROCESS SIMULATOR**





## **NEEDS FOR PROCESS SIMULATOR**

#### Genericity

- Ability to model any industrial process (e.g., electricity distribution, chemical factory)
- Not focused only on a single application type
- Easily customizable

#### Fine grain modeling

- Not our priority at the moment /!\
- We focus on basic functionalities (e.g., "opening a valve" and not on the intrinsic properties of the valve), yet finer modeling can be required.

#### ■ Ability to communicate /!\

- ▶ With interface cards (thus with PLC, SCADA, etc)
- ► In TCP/RTU Modbus
- ▶ With data bases, API, etc
- With GPIOS
- Ability to run on various systems
  - ► Raspberry PI, computer, etc



## **EXISTING PROCESS SIMULATORS**

#### A real-time (time stepped simulation is needed)

Process dynamics simulated at real-life time not computer time

#### Tested and validated simulators :

#### Modelica (with real-time) toolbox

Native UDP communication implementation

#### ► Factory I/O

- Engine I/O library -> interface provided
- ► Home I/O
  - Engine I/O library -> interface provided
- Matlab/Simulink
  - Native UDP communication implementation



## **PROGRAM AND CONFIGURATION**

#### Built on Atollic TrueSTUDIO® for STM32 V9.2.0

#### One can configure

- Mode (PLC or protection relay)
- Network configuration
- Background task
- Mode (gics.c)
- static int config\_mode = GICS\_API; // configure for PLC
- static int config\_mode = GICS\_IED; // configure for protection relay



## **NETWORK CONFIGURATION**

- By default a card is idenfyied by a RACK\_ID and a CARD\_ID
- Ethernet config (ethernetif.c)
- MACAddr[0] = 0x02;
- MACAddr[1] = 0x61;
- MACAddr[2] = 0xc5;
- MACAddr[3] = RACK\_ID;
- MACAddr[4] = 0x00;
- MACAddr[5] = CARD\_ID;
  - You can use any values instead default ones but respect the rules :
    - Each card into the same network has a different MACAddr
    - First bit on byte O has to be O (otherwise it is a broadcast address).





## IP CONFIGURATION

File lwip.c

- IP\_ADDRESS[0] = 10;
- IP\_ADDRESS[1] = 10;
- IP\_ADDRESS[2] = 100;
- IP\_ADDRESS[3] = RACK\_ID\*16+GICS\_ID;
- NETMASK\_ADDRESS[0] = 255;
- NETMASK\_ADDRESS[1] = 255;
- $NETMASK_ADDRESS[2] = 0;$
- NETMASK\_ADDRESS[3] = 0;

 $GATEWAY\_ADDRESS[0] = 10;$ 

GATEWAY\_ADDRESS[1] = 10;

GATEWAY\_ADDRESS[2] = 255;

 $GATEWAY\_ADDRESS[3] = 254;$ 



## **CARD COMMUNICATION PROTOCOL**

- Simple requests derived from Modbus protocol
- Frame data defined in gics.h
  - typedef struct GICSTransaction {
  - unsigned char function;
  - unsigned char magic;
  - unsigned short length;
  - unsigned short data[255];
  - GICSTransaction;
- Elementary Functions
  - #define GICS\_READ 0x01
  - ► #define GICS\_WRITE 0x02
  - #define GICS\_DISCRETE 0x04
  - ► #define GICS\_ANALOG 0x08
  - ► #define GICS\_DA 0x10

always GICS\_MAGIC 0xd0



## **CARD COMMUNICATION PROTOCOL**

#### Request functions

- Combination of READ/Write and data type
- Read Analog/digital = GICS\_READ + GICS\_DA = 0x11
- Write Analog/digital = GICS\_WRITE + GICS\_DA = 0x12

#### Anwsers

- Write requests are not answered
- Read answers have a magic number 0xd1
- Default UDP port = 2015
- A Wireshark dissector exists



DATA EVOLANOE	No.	Time	Source	Destination	Protocol	Length	Info		^
DATA EXCHANGE		10.000000	10.10.20.200	10.10.100.50	G-ICS	6	4 Request	: Write Dig	italAnalog
		2 0.099809	10.10.20.200	10.10.100.50	G-ICS	4	5 Request	: Read Digi	talAnalog
		3 0.101450	10.10.100.50	10.10.20.200	G-ICS	6	4 Answer	Read Digit:	alAnalog 🗸
	<							2 10.20	
Write request (D/A)	> F > E > U > U	rame 1: 64 by thernet II, S nternet Proto ser Datagram -ICS Protocol request: 0x1 len: 18 ( Da nalog Inputs Analog In1:5 Analog In2:5 Analog In3:5 Analog In5:5 Analog In6:5 Analog In7:5	tes on wire (512 rc: Dell_39:ff:9 col Version 4, S Protocol, Src Po Data 2 (Write Digita) ata Len ) to PLC 512 512 512 512 512 512	2 bits), 64 bytes 94 (c8:f7:50:39: 97 c: 10.10.20.200 97 crt: 51306, Dst F 1Analog)	s captur ff:94), 0, Dst: Port: 20	ed (512 Dst: 02 10.10.1	! bits) d !:61:c5:0	on interfac 03:00:02 (0	e \Device\NP 2:61:c5:03:00
		Analog Ins:5	12						
	D	igital Inputs	TO PLC:0000 000	1 0000 0000					
	<					j.			>
	00	aa a2 61 c5 6	03 00 02 c8 f7	50 39 ff 94 08 0	0 45 00	· a · ·	pg.	F -	
	00	10 00 32 1c	34 00 00 80 11	00 00 0a 0a 14 c	8 0a 0a	-2-4			
	00	20 64 32 68 1	5a 07 df 00 1e	8d 3d 12 d0 00 1	2 02 00	d2 - i	<mark>_</mark> .		
	00	30 02 00 02 0	00 02 00 02 00	02 00 02 00 02 0	0 01 00		**** ***		
	0	G-ICS Protocol (g-ic:	s), 22 byte(s)			Paquets: 2	24 · Affichés: 2	4 (100.0%)	Profile: Default



## **DATA REQUEST**

#### Read D/A request

No.	Time	Source		Destination		Protocol	Length	Info				^
1	0.000000	10.10.20	. 200	10.10.	100.50	G-ICS	64	Request	Write	Digita	lAnalo	g
_ 2	0.099809	10.10.20	. 200	10.10.	100.50	G-ICS	46	Request	Read [	Digital	Analog	3
- 3	0.101450	10.10.10	0.50	10.10.	20.200	G-ICS	64	Answer	Read D:	igitalA	nalog	~
<												>
> Fra	me 2: 46 b	ytes on wir	e (368 b	its), 4	46 byte	s captur	ed (368	bits) o	n inte	rface \	Device	NPF
> Eth	ernet II,	Src: Dell 3	9:ff:94	(c8:f7	:50:39:	ff:94),	Dst: 02	:61:c5:0	3:00:0	2 (02:6	1:c5:0	3:00
> Int	ernet Prot	ocol Versio	n 4, Src	: 10.10	0.20.20	0, Dst:	10.10.10	00.50				
> Use	r Datagram	Protocol,	Src Port	: 5130	7, Dst	Port: 20	15					
> Use ~ G-I	r Datagram CS Protoco	Protocol, : l Data	Src Port	: 5130	7, Dst	Port: 20	15					
> Use ~ G-I r	r Datagram CS Protoco equest: 0x	Protocol, S l Data 11 (Read Di	Src Port gitalAna	: 5130	7, Dst	Port: 20	15					
> Use ~ G-I r 1	r Datagram CS Protoco equest: 0x en: 0 ( Da	Protocol, : l Data 11 (Read Di ta Len )	Src Port gitalAna	: 5130 log)	7, Dst	Port: 20	15					
> Use ~ G-I r 1	r Datagram CS Protoco equest: 0x en: 0 ( Da	Protocol, S l Data 11 (Read Di ta Len )	Src Port gitalAna	: 5130) log)	7, Dst	Port: 20	15					>
> Use ~ G-I r 1 < 0000	r Datagram CS Protoco equest: 0x en: 0 ( Da 02 61 c5	Protocol, 3 l Data 11 (Read Di ta Len ) 03 00 02 c8	Src Port gitalAna 8 f7 50	: 5130 log) 39 ff	7, Dst 94 08 0	Port: 20 00 45 00	15 •a•••	··· P9-	E.			>
> Use ~ G-I r 1 < 0000 0010	r Datagram CS Protoco equest: 0x en: 0 ( Da 02 61 c5 00 20 1c	Protocol, 3 1 Data 11 (Read Di ta Len ) 03 00 02 c8 35 00 00 80	Src Port gitalAna 6 f7 50 9 11 00	: 5130 log) 39 ff 00 0a	7, Dst 94 08 0 0a 14 0	Port: 20 00 45 00 28 0a 0a	15 .a	··· P9-	•••E•			>



Destination

10.10.100.50

#### **Cybersecurity Institute**

64 Request Write DigitalAnalog

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Info

Length

Protocol

G-ICS

## DATA REQUEST

Time

10.000000

Source

10.10.20.200

08 01 07 fe 07 fe 08 00 08 01 08 04 08 00 00 00

No.

0030

#### Read D/A Answer

je:	20.	0998	09		10	10	. 20	200	10	.10	.100	.50	C	5-I	cs	46	Requ	est	Rea	d Di	gita	lAna	log	
	30.	1014	50		10.	. 10	. 100	9.50	10	.10	.20.	200	0	5-I	cs	64	Answ	er R	lead	Dig	ital	Anal	og	~
<																							>	
>	Frame	3: 6	54 Ł	oyte	s o	n w	ire	(51)	2 bit	s),	64	byte	es c	ap.	tured	(512	bits	) or	n in	terf	ace	\Dev	vice\	NP
>	Etherr	et 1	Ί,	Src	: 0	2:6	1:c	5:03	:00:0	2 (6	92:6	1:c	5:03	3:0	0:02),	, Dst	: Del	1_39	e:ff	:94	(c8:	f7:5	0:39	:f
>	Interr	net F	rot	toco	1 V	ers	ion	4, 5	Src:	10.1	10.1	00.5	50,	Ds	t: 10.	.10.2	0.200							
>	User D	atag	gran	n Pr	oto	col	, s	rc Po	ort:	2015	5, D	st F	Port		51307									
×	G-ICS	Prot		ol D	ata	G.																		
	req	uest	: 0)	x <b>1</b> 1	(Re	ad	Dig	gital	Analo	g)														
	len	: 18	(	Data	a Le	en )	)																	
~	Analog	; Out	put	ts f	rom	PL	C																	
	Ana	log	Dut:	1:20	929																			
	Ana.	Log	Out	2:20	949																			
	Ana	Log	Dut	3:20	946																			
	Ana.	Log (	Dut	4:20	946																			
	Ana	log	Out!	5:20	948																			
	Ana	log	Dut	6:20	949																			
	Ana	log	Dut	7:20	952																			
	Ana.	log	Dut	8:26	948																			
	Digita	1 00	itpu	uts	fro	m P	LC:	0000	0000	000	0 00	000												
<																								>
0	999 6	8 <del>f</del> 7	50	30	ff	94	82	61	c5 83		02	08	99	45	88	pq			F					
0	010 0	0 32	00	07	00	00	ff	11	Ze af	5 0a	0a	64	32	0a	Øa	.2			d2 -					
a	020 1	4 c8	07	df	68	6b	00	1e	50 8	7 11	d1	00	12	87	ed		k··· F							



## **PROGRAM MAIN LOOP (GICS.C)**

#### Request decoding and DI/O

- gics\_udp\_server\_receive\_callback
- Interrupt DA/C updater
  - Update\_DAC\_Handler
- Analog I/O : directly handled via memory transfer
- Protection relay signal adapter (sinusoïdal signals generation)
  - gics\_update\_DAC\_vars
- A/D and D/A conversion rules
- DAC are 10 bits precision. Therefore DAC inputs are 0...1023 for a +/- 10V output
- ADC are 12 bits precision. For à +/-10V input, the output will vary between 0 and 4095
- Note that a small bias is to be expected

## Classwork

- Synthesize a UDP frame to
  - Request data from GICS card
  - Write the set of boolean {1,0,1,0,1,0,1,0} to the output no. 1 8 of the GICS card

# Lecture 02: Process monitoring









Asean-Factori 4.0

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Synoptic: essential function of the supervision, provides a synthetic, dynamic and instantaneous representation of all the means of production of the unit



18 - JMT





## Curves:

- gives a graphical representation of different process data
- gives the tools to analyze the historical variables







## Alarms

- Calculates in real time the conditions for triggering alarms
- Displays all alarms according to priority rules
- gives management tools
- ensures the recording of all the steps of the alarm processing







b		Device test and o	diagnostic	s
1	Name	Description	State	
4	Buzzer	Tests the buzzer	Passed	
4	COM1	Tests the serial port	Warning	
4	Device information	Reads device information	Passed	
4	Fan	Tests fans in PC and panels	Passed	
4	Firmware	Reads firmware information	Passed	
1	Key	Tests device buttons and panel keys	Failed	<b>o</b> -
~	LED	Tests device LEDs and panel LEDs	Passed	
1	Network ETH1	Tests the network interface	Warning	
4	Network ETH2	Tests the network interface	Warning	
1	RAM	Tests the main memory	Running	
vele	es: 11 Passed: 7 War	nings: 3 Failed: 1 Skipped: 0		

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Circumscribe the cause of the feared event (cause of the incident) Limit the impact of the event, protect (consequences) Be able to assess the system after the incident: repair, reconfigure (total and partial redundancies) Reconstruct, recover the system: time required for it to be operational again, what happens and what are the recovery steps? (Activity Return Plan)

Other related aspects: robustness, resilience (ability to maintain the system as well as possible in a situation of "attacks")



# **Alarms detection**



- TP (true positive) corresponds to correctly identified alarms
- FP (false positive) corresponds to authentic behavior identified as faulty
- TN (True Negative) corresponds to the correct rejection of authentic behavior
- FN (False Negative) corresponds to undetected failures
- Two metrics are used to evaluate the performance of alarm detection
  - True Positive Rate TPR=TP/(TP+FN)

=> 1 if no False Negative

False Positive Rate FPR=FP/(FP+TN)

=> 0 if no False Positive

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Historicization of the process:

- Allows the saving of timestamped events (selective archiving)

- provides search tools in the archived years

provides the possibility to run the synoptic again with archived data (replay function)
allows to keep a validated

trace of critical data (traceability of production data)



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Asean-Factori 4.0





Management of production lines and recipes:

- Provides a tool for managing production batches
- Manages the parameters of the machines for each batch (recipes)



#### Asean-Factori 4.0

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- Select and study a manufacturing process and then design and list its monitoring parameters

# Lecture 03: Process control

## **Networked Control Systems (NCS)**





## Quality of Service and Quality of Control for Safer Networked Control Systems (SafeNCS)

Communication networks are more and more used in control-based applications with real-time and/or critical constraints. Communication and Control aspects need so to be seen from a global point of view. Communication (networks) is shared between various applications, and some aspects such as wireless communication and mobility needs to be taken into account in the design of SafeNCSs. Two examples of SafeNCS can be:



A drone with very strict real-time constraints => control-oriented,

A pacemaker which is remote-supervised from time to time, the infrastructure should protect strongly the integrity of supervision => security-oriented

Cyber-security of industrial systems is now a crucial issue => impact of cyber-security to the safety of networks

In both cases, focusing on control and/or on security aims at guaranteeing **safety** 




# Quality of Service and Quality of Control for Safer Networked Control Systems (SafeNCS)

Quality of service aims at guaranteeing the best communication aspects, focusing mainly on:

security aspects: to protect the communication, in order to protect confidentiality of exchanges, integrity of data and control, authentication of actors of the SafeNCS. availability of the network, for the considered control application, by allowing the network to control the distribution of throughput as a function of the requirements of the applications (priorities of applications). For that, we can study the network protocols and mechanisms as well as the infrastructure.

Quality of control deals with the need of "automatic control" from the point of view of control, diagnosis, supervision...

stability which means to guaranty the controllability of the system, despite the potential unavailability of the network

performance which should be the best as possible in a varying environment, taking account of minimal levels of security, stability and safety.

The presentation will present the problems, propose some approaches and results, and orientations concerning the study of Safe Networked Control Systems

## Synthesis on the concepts

 Dependability : Confidence in the system to ensure its mission without risk (or with a risk management)

=> Co-design approach (Network QoS ⇔ System QoC)

- 2. Functional safety: part of the overall safety that depends on a system or equipment operating correctly in response to its inputs [IEC]
- 3. Cyber-security: Cyber security is the protection of systems, networks and data in cyberspace [www.itgovernance.co.uk]
- 4. Networked Control Systems: Control System closed through a network
- 5. Complex systems, infrastructure, distributed systems
- 6. Embedded system, autonomous system, connected objects, IoT
- 7. ICS : Industrial Control Systems
- 8. Cyber-physical systems (CPS): Marrying physicality and computation [persyval-lab.org]
- Our interest: To analyse CPS from the point of view of the potential impact of the system in the physical world (dependability point of view) due to a cyber-attack (attack in the digital world) and define the ways to protect it

# Safety/Dependability level (RAMS) of a networked based system, wired networks

#### Steering by wire

- Probability that the vehicle doesn't turn, when it is requested
- Probability that the vehicle turns unexpectedly

#### Difficult evaluation

- Network more complex than a set of point-to-point links
- Network more complex than a delayed system
- Network-system Interaction

#### Drive shaft



X by wire, steering by wire

# Dependability of networked-based systems (Wireless Network)

1<sup>st</sup> vehicle (controlled)



X by wire, brake by wire

#### Braking Function

- First vehicle
  - Probability that the vehicle does not brake when it is asked for
  - Probability the vehicle brakes without any request

#### Existing system

Verification model (formal approach, Monte-Carlo simulations)

#### Non existing system

Design model: « ideal » model + dependability constraints

2<sup>nd</sup> vehicle (following the 1<sup>st</sup> one)



Automated driving (virtual train)

#### Second vehicle

 Probability that it receives a braking information from the 1st vehicle, if everything is correct for the first vehicle

- ...

JMT - Tutorial - Cyber-Security of Cyber-Physical Systems- Asean-Factori- 2022

#### Embedded system (Embedded wired network + Remote wireless communication) with strong dynamics

#### **Drone-helicopter**

#### Definition of the mission

Weak dynamics (normal displacement straight ahead) Strong dynamics (ex : slaloms between trees) Disturbed communication environment (e.m. disturbances, trees...)

#### Quality of service of the network

High in critical situation High in strong dynamic situations (if remote-control) Lower other time







# Example: control specification

- Sequential behaviour: finite state machine, determitist automata, graphs with states and transitions
- Outputs are function of inputs and internal state of the automaton



- Process: mixture (blending) between two products in a tank containing a blender
- Mixture between Product 1(yellow) and product 2 (blue)The blender is actuated by an
- engine (M)
- At the end the mixed product (green) will be evacuated through the valve VE

FACTORI



- Time T0
- When E switches from 1 to 0 (descending front), which means the tank is empty, this begins a new sequence



PLC – UGA - Asean-Factori - JMT

> Factori

4.0





 Once the P1 sensor (level) is reached => Transition 1: VP1 is closed, and VP2 (blue product) is open to add blue product









 Once the P2 sensor (level) is reached => Transition 2: VP2 is closed, and M (blender) is switches on









FACTORI

4.0



 At the end of the mixing, we stop the engine, and open VE, which is the evacuation valve.



PLC – UGA - Asean-Factori - JMT





The tank is emtying, P2 is deactivated





Then P1 is deactivated



> Factori

4.0





- VE is closed, the cycle is over
- We can begin again





• Implement the ladder diagram of the mixture problem

# 1501324 Sequence Control and PLC

Program: Bachelor program in Computer EngineeringCredit: 3(2-2)Lecture: 30 HoursLab: 30 Hours



This course has been modified in the framework of an Erasmus + project: Asean Factori 4.0 Across South East Asian Nations: From Automation and Control Training to the Overall Roll-out of Industry 4.0

609854-EPP-1-2019-1-FR-EPPKA2-CBHE-JP

2<sup>nd</sup> Semester, Academic Year: 2023

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Lect. Chayapol Kamyod, Ph.D.



# Lab 01: Process monitoring via HMI

# Human-Machine Interface



HMI provides a method of displaying information and obtaining inputs, modeling the control system as a whole. HMIs do not typically provide any way to modify the logic program

# Add an HMI to the project

Double click the "Add new device" command

Project tree	
Devices	
Ē	
<ul> <li>Project10</li> </ul>	
💕 Add new device	
💑 Devices & networks	
Diagram Distance [CPU 1512C-1 PN]	
Ungrouped devices	
🕨 🚟 Security settings	
🕨 🔀 Cross-device functions	
🕨 🙀 Common data	
Documentation settings	
🕨 词 Languages & resources	
Online access	
🕨 🣴 Card Reader/USB memory	

Add new device			
Device name:			
		1	
HMI_1		]	
		Device:	
	SIMATIC Basic Panel		
	▼ Im SIMAIIC Comfort Panel		
Controllers	■ 4 Display		
	TR700 Comfort		
	64V2 124-06C01-0AX0		TP700 Comfort
	TP700 Comfort INOX PCT		
	TP700 Comfort INOX PCT Port	Article no.:	6AV2 124-0GC01-0AX0
HMI	TP700 Comfort Portrait	Version	16000
	TP700 Comfort Outdoor	version.	10.0.0.0
	TP700 Comfort Outdoor Po	Description:	
	🕨 🔚 KP700 Comfort	7.0" TFT displa	y, 800 x 480 pixels, 16M colors;
	🕨 🛅 9'' Display	Touch screen;	1 x MPI/PROFIBUS DP, 1 x
	🕨 🛅 12'' Display	and RT/IRT sup	port (2 Ports); 2 x Multimedia card
PC systems	🕨 🛅 15'' Display	slot, 3 x USB	
	🕨 🛅 19'' Display		
	🕨 🛅 22'' Display		
	🕨 🛅 SIMATIC Mobile Panel		
	HMI SIPLUS		
Select the	required HMI device		
Sciect the	required min device		
	< III >		
Start device wizard			OK Cancel







Project tree 🔲 🖣	roject10 → Devices & networks	٥
Devices	🐺 Topology view — 🚠 Network view — 👔 Device vie	w
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Retwork Retwork Retwork Connections Red Connections VPN TeleControl	
<ul> <li>Project10</li> <li>Add new device</li> <li>Devices &amp; networks</li> <li>PLC_1 [CPU 1512C-1 PN]</li> <li>HML_1 [TP700 Comfort]</li> <li>Device configuration</li> <li>Online &amp; diagnostics</li> <li>Runtime settings</li> <li>Screens</li> <li>Add new screen</li> <li>Root screen</li> <li>Root screen</li> <li>Root screen</li> <li>Screen management</li> <li>Screen management</li> <li>HMI tags</li> <li>Show all tags</li> <li>Add new tag table</li> <li>Default tag table [2]</li> <li>Connections</li> <li>Hold alarms</li> <li>Recipes</li> <li>Historical data</li> <li>Scripts</li> <li>Scheduled tasks</li> <li>Cycles</li> <li>Reports</li> <li>Text and graphic lists</li> <li>User administration</li> <li>Ungrouped devices</li> <li>Security settings</li> <li>Consections</li> <li>Common data</li> <li>Documentation settings</li> <li>Name</li> </ul>	PLC_1 Connection Interface 1[x] Connection Inte	



	Project tree	III 🖣 🖡	Project10 → HMI_1	I [TP700 Comfor	t] 🕨 HMI tags 🕨	Default tag ta	ble [2]		
	Devices								
			🔊 🕞 🖶 💫						
	_		Default tag tab	e					
	▼ 🕞 HMI 1 [TP700 Comfort]		Name .	Data type	Connection	PLC name P	C tag Address	Access mode	Acquisition cycle
	Device configuration		Tag Screen	Number UInt	<nternal tag=""></nternal>	<	Und		1 s
	😵 Online & diagnostics		Add new>		j.				
	Runtime settings		11						
	Screens			The first	thing to d	o is to m	anage HI	MI's tag table	د
	🕨 📴 Screen managemen	t 🖌		THE HISE	tining to u		anage m	vii 5 tag tabit	•
	🔻 🔚 HMI tags								
	🗞 Show all tags								
	🚽 📑 Add new tag tabl								
Project tree	💥 Default tag table	[2]	Discrete alarms	Analog alarr	ns Logging ta	ags			
Devices									
_	Default tag table								
▼ Project10	Name 🔺	Data type	Connection	PLC name PLC ta	q Address Acces	s mode	Acquisition cyc	le Logged Source.comr	ment
Add new device	Tag_ScreenN	lumber Uint	⊲nternal tag>	<und.< td=""><td></td><td></td><td>1 s</td><td></td><td></td></und.<>			1 s		
Devices & networks	Tag_ScreenN	lumbe Ulnt 🔳	<nternal tag=""></nternal>	. 48			• 1 s		
PLC_1 [CPU 1512C-1 PN]	<add new=""></add>								
▼ 🛅 HMI_1 [TP700 Comfort]					PLC_1 [CPU 1512C	-1 PN]		$ \mathbf{Y} $	
🛐 Device configuration					Software units		Name	Data type	Address
况 Online & diagnostics					Program blocks	5	None		
🍸 Runtime settings					Iechnology obj BLC tags	ects	В	Bool	%I10.1
▼ [] Screens	Discusto al anno	Anglengler			Default tags	table 🛛	E	Bool	%I10.2
Add new screen	Discrete alarms	Analog alarn	ns Logging tag	gs	Delaut lag		Mix	Bool	%Q4.2
						• 🖅	P	Bool	%I10.0
When the <add ne<="" td=""><td>w&gt; is clicked, the r</td><td>next is to l</td><td>browse and</td><td>l link</td><td></td><td></td><td>V1</td><td>Bool</td><td>%Q4.0</td></add>	w> is clicked, the r	next is to l	browse and	l link			V1	Bool	%Q4.0
to a PIC's tag							V2	Bool	%Q4.1
						•	VE	Bool	%Q4.3
Default tag table [3]				(	1111				
2 Connections					Showall				
🖂 HMI alarms					Showah				



Now, it's time to work with the screen. As the current sample is not complicate, the root screen is only used.





Circle_1 [Circle]							Rise Properties	🗓 Info 🔒 🗓 Diagnostics	
Properties Animation	s Events T	exts							
	Appearance								
Overview V Coverview V Display	Tag					Туре			
💣 Add new animation	Name:				■	Range			
Appearance 📕	Address:					<ul> <li>Multiple bits</li> </ul>			
Movements						Single bit			
•	Range 🔺	Background color	Border color	Flashing					
-	<add new=""></add>								
	_								_
	The app	earance set	ting is no	w available					

Circle_1 [Circle]			Roperties	🗓 Info 👔 🖳 Diagnostics 👘 💷 🗏
Properties Animations	Events	Texts		
A	ppearance			
Overview	Tag	Туре		
Add new animation	Name:	. 📃 🔜 🔪 💽 Range		
Appearance	Address:	@DiagnosticsIndicatorTag		<u>^</u>
Movements		a Draining		
4	-	Tevel 1		
	Range 🔺	Tevel 2		
-	<add new=""></add>	Mixer		
	-	Tag_ScreenNumber		
	-	Talve 1		
	-	Valve 2		~

Browse and select the desired HMI's tag. In this case, select the 'Level 2' tag.







Manually edit the text to 'Label 2' which is matching its tag

Now, repletely add circles and labels to show all available HMI's tags

Don't forget to link each added circle to its correlated tag





#### **PLC Simulator**

# Simulating an HMI

- HMI simulation is an independent session that sync with the targeted session of PLC simulation
- Actually, it doesn't matter which one is running first
  - Personally, running a PLC simulation first is recommended
- In this case, run the PLC simulation first, and make sure the operation is correct
- Then, run the HMI simulator, they will automatically synchronized each others



#### 

# Exercise

- Modify the problem to contain another mixer, and monitoring its status in the HMI
### Lab 02: Process control via HMI

#### Process control via HMI



But, the output of the PLC can be manipulated by the HMI in order to override or suspense the normal process if there are necessary factors

### Start/Stop the process from HMI

- Add a control switch to start and stop the whole process
- V1, V2, Mixer and VE are off as the switch is off
- VD or ditching valve is added to ditch the remaining liquid in the tank as the process is forced to stop
  - VD is on as long as the process is forced to stop







Project tree			Proje	ct1	0	1512C-1 PN]	PLC tags	•	Default tag	table	[68]			
Devices														
1 de la companya de l	•		<u></u> ₩.	s'e	🖻 🛃 📽 🛍									
		Default tag table												
Project10		^			Name		Data type		Address		Retain	Acces	Writa	Visibl S
📑 Add new device			1	-	Р		Bool		%I10.0			<b></b>		<b></b>
🛗 Devices & networks			2	-	В		Bool		%110.1			<b>~</b>	<b></b>	<b></b>
PLC_1 [CPU 1512C-1 PN]			3	-	E		Bool		%I10.2			<b>~</b>	<b></b>	<b></b>
Device configuration			4	-	V1		Bool		%Q4.0			<b>~</b>		<b></b>
😧 Online & diagnostics			5	-	V2		Bool		%Q4.1			<b>~</b>	<b></b>	<b></b>
Software units	1	≡	6	-	Mix		Bool		%Q4.2			<b>~</b>	<b></b>	<b></b>
🕨 🔂 Program blocks			7	-	VE		Bool		%Q4.3			<b>~</b>	<b>~</b>	
🕨 🙀 Technology objects			8	-	VD		Bool		%Q4.4	-		<b>~</b>	~	<b></b>
External source files		1	9		<add new=""></add>							<ul> <li>Image: A start of the start of</li></ul>	<ul> <li>Image: A start of the start of</li></ul>	Image: A start of the start
🔻 🎑 PLC tags														
lange Show all tags														
📑 Add new tag table		X												
💥 Default tag table [68]														

With no HMI tags for the added components, return back to the PLC - Add new valve 'VD' and set its address

The user command from the HMI is not input and output of the PLC.

- So, its tag is not I (input) or Q (output)

Project tree		Proje	ect1(	) ▶ PLC_1 [CPU 151	2C-1 PN]  PLC tags	Default tag	table [69]				
Devices											
	1	<u></u> ≝∛ :	ŝĝ.	🔿 🛃 😤 🛍							
	_	D	)efai	ult tag table							
Project10	^			Name	Data type	Address	Retain	Acces	Writa	Visibl	Supervis
💣 Add new device		1	-00	P	Bool	%I10.0				<b></b>	
n Devices & networks		2	-00	В	Bool	%I10.1			<b></b>	<b>~</b>	
PLC_1 [CPU 1512C-1 PN]		3	-00	E	Bool	%I10.2			<b></b>	<b></b>	
Device configuration		4	-00	V1	Bool	%Q4.0			<b></b>	<b>~</b>	
🖳 Online & diagnostics		5	-00	V2	Bool	%Q4.1			<b></b>	<b>~</b>	
Software units	≡	6	-00	Mix	Bool	%Q4.2			<b></b>	<b>~</b>	
🕨 📴 Program blocks		7	-00	VE	Bool	%Q4.3			<b>~</b>	<b>~</b>	
🕨 🚂 Technology objects		8	-00	VD	Bool	%Q4.4			<b>~</b>	<b>~</b>	
External source files		9	-00	on	Bool	™0.0		$\checkmark$	<b></b>	<b>~</b>	
🔻 🚂 PLC tags		10		<add new=""></add>		1				•	
lange Show all tags							Operand ident	ifier: M			
📑 Add new tag table							Operand t	ype:			
💥 Default tag table [69]							Addr	ess: 0			
PLC data types		Y					Rit pure	bari 🚺			
Watch and force tables							bit num			-	
🕨 🙀 Online backups											
🕨 🔄 Traces											X

A tag named 'on' is added to set the status of the process to be on or not.

- As it is to be received from the HMI, it is not input or output.

- So, just put it in PLC's memory (M)
- Its value is to be controlled by the HMI

### Put the control variable into the ladder



### Config the HMI to control the tag

Project tree	Project	10 • HMI_1 [TP700 Comfort]	→ HMI tags → Def	ault tag table [	11]	
Devices						
1 III III III III III III III III III I	¥ 🗄	• 🗄 🐁				
	Def	ault tag table				
▼ 📄 HMI_1 [TP700 Comfort]		Name 🔺	Data type	Connection	PLC name	PLC tag A
Device configuration		Draining	Bool	HMI_Connectio	PLC_1	E
😨 Online & diagnostics		Level 1	Bool	HMI_Connectio	PLC_1	P
Y Runtime settings		Level 2	Bool	HMI_Connectio	PLC_1	В
▼ Creens		Mixer	Bool	HMI_Connectio	PLC_1	Mix
💕 Add new screen		Tag_ScreenNumber	UInt	⊲nternal tag>		<undefined></undefined>
Root screen		Valve 1	Bool	HMI_Connectio	PLC_1	V1
🔸 🕨 🔯 Screen management		Valve 2	Bool	HMI_Connectio	PLC_1	V2
🔻 🔚 HMI tags		Valve E	Bool	HMI_Connectio	PLC_1	VE
lange Show all tags		Valve D	Bool	HMI_Connectio	PLC_1	VD
💕 Add new tag table		on	Bool	HMI_Connectio	PLC_1	on
📲 Default tag table [11]		<add new=""></add>				

First, update the HMI tag table with two new tags from the PLC Then, link these new tags to the screen components

## Link the 'on' tag to the switch





As it is a switch, the tag connections is enable by default

Config its property and tag if they're not automatically set by previous setting





	Project tree		SIM	tab	le_1					_ <b>=</b> = ×
			<b>*</b>	2	🔊 🗣 🖶 🖣	01				-
				- 1	Name	Address	Display format	Monitor/Modify value	Bits	Co
	Project10	<	-	•	"IEC_Timer_0_DB		Time	T#5S		T#
	Diamond Content in the second seco	<b>~</b>	-		"IEC_Timer_0_DB		Time	T#OMS		T#
	🔻 🔚 SIM tables		-		"IEC_Timer_0_DB		Bool	FALSE		FA
	📑 Add new SI		-	•	"IEC_Timer_0_DB		Bool	FALSE		FA
	📑 Browse		-		"P":P	%I10.0:P	Bool	FALSE		FA
	SIM table_1		-		"B":P	%I10.1:P	Bool	FALSE		FA
	🔻 📴 Sequences		-	•	"E":P	%I10.2:P	Bool	FALSE		FA
	📑 Add new se		-		"V1"	%Q4.0	Bool	FALSE		FA
	B Sequence_1	>	-	•	"V2"	%Q4.1	Bool	FALSE		FA
	Event tables		-	•	"Mix"	%Q4.2	Bool	FALSE		FA
Add new tag to the SIM table:			-	•	"VE"	%Q4.3	Bool	FALSE		FA
					"on"	%M0.0	Bool	FALSE		FA
on and VD					"VD"	%Q4.4	Bool	TRUE		🗹 FA
				<						>
							A			

The tag 'on' is initially off, all other tags but 'VD' are off



#### Now, compile and run the HMI simulator



HMI are being compared

As the process is initially off, any device but 'Valve D' is off

#### 

#### Exercise

- Try to put the "Pause" button to just pause the process without flooding the mixed liquid

### Lab 03: Emulation card



#### HARDWARE IN THE LOOP SYSTEM

#### Home made electronic interface card



#### 24 sensors and 24 actuators

- 16 digital inputs / 16 digital outputs
- 8 analog inputs / 8 analog outputs
- ► Less than 500€
- Reasonable timing performance ( < 10 ms response time)</p>
- Easily chain (Ethernet addressing)



#### Configuration







outputs

### Problem: Simple inputs and outputs

- Input 1 on  $\rightarrow$  Output 1 on
- Input 2 on  $\rightarrow$  Output 2 on
- Input 3 on  $\rightarrow$  Output 3 on
- Input 4 on  $\rightarrow$  Output 4 on
- Input 1 off  $\rightarrow$  Output 1 off
- Input 2 off  $\rightarrow$  Output 2 off
- Input 3 off  $\rightarrow$  Output 3 off
- Input 4 off  $\rightarrow$  Output 4 off

### Configuration

# • Create a project with PLC of firmware version 2.5







### Programming the PLC



### Compile and load config/code to PLC

In practical, at this point, the code and config are to be loaded to the destination PLC.



If there is no error, the TIA Portal and the targeted PLC are synchronize. The PLC will run automatically but the Portal can monitor and debug.





xtended downloa	ad to device					×
	Configured acce	ess nodes of "PLC_1"				
	Device	Device type	Slot	Interface type	Address	Subnet
	PLC_1	CPU 1512C-1 PN	1 X1	PN/IE	10.1.29.191	PN/IE_1
	<u> </u>					
		Type of the PG/PC inter	face:	PN/IF		<b>•</b>
		PG/PC inter	face:	Realtek PCIe G	bE Family Controlle	•r 🔻 🗑 🔯
		Connection to interface/sul	onet:	Direct at slot '1 2	x1'	
		1st gate	wav:			
			-			
	Select target de	evice:		[	Show all compatib	le devices
	Device	Device type	Interf	ace type Add	dress	Target device
	-	-	PN/IE	Acc	ess address	-
Flash LED						
						<u>S</u> tart search
Online status inform	nation:			(	Display only erro	or messages
					Lo	ad <u>C</u> ancel

	Configured acc	ess nodes of "PLC_1"				
	Device	Device type	Slot	Interface type	Address	Subnet
	PLC_1	CPU 1512C-1 PN	1 X1	PN/IE	10.1.29.191	PN/IE_1
		Type of the PG/PC int	erface:	PN/IE		•
		PG/PC int	erface:	Realtek PCIe G	bE Family Controller	
		Connection to interface/s	ubnet:	Direct at slot '1	x1'	
		1st as	teway			
	Select target de	evice:		[	Show all compatible	e devices
	Select target de Device	Device :	Interfa	ice type Add	Show all compatible dress	e devices Target device
	Select target de Device PLC_1	evice: Device type CPU 1512C-1 PN 	Interfa PN/IE PN/IE	ice type Add 10. Acc	Show all compatible dress 1.29.191 ess address	e devices Target device PLC_1 
	Select target de Device PLC_1 	evice: Device type CPU 1512C-1 PN 	Interfa PN/IE PN/IE	ice type Add 10. Acc	Show all compatible dress 1.29.191 cess address	e devices Target device PLC_1 
ash LED	Select target de Device PLC_1 	evice: Device type CPU 1512C-1 PN 	Interfa PN/IE PN/IE	ice type Add 10. Acc	Show all compatible dress 1.29.191 cess address	e devices Target device PLC_1 
ilash LED	Select target de Device PLC_1 	evice: Device type CPU 1512C-1 PN 	Interfa PN/IE PN/IE	ice type Add 10. Acc	Show all compatible dress 1.29.191 cess address	e devices Target device PLC_1 
ash LED	Select target de Device PLC_1 	evice: Device type CPU 1512C-1 PN 	Interfa PN/IE PN/IE	Ice type Add 10. Acc	Show all compatible dress 1.29.191 cess address	e devices Target device PLC_1  <u>S</u> tart search
Flash LED	Select target de Device PLC_1 	evice: Device type CPU 1512C-1 PN 	Interfa PN/IE PN/IE	Ice type Add 10. Acc	Show all compatible dress 1.29.191 cess address Display only error	e devices Target device PLC_1  <u>S</u> tart search message
a status informatio	Select target de Device PLC_1 	evice : Device type CPU 1512C-1 PN  evice	Interfa PN/IE PN/IE	ice type Add 10. Acc	Show all compatible dress 1.29.191 cess address Display only error	e devices Target device PLC_1  <u>Start search</u> rmessager
Flash LED	Select target de Device PLC_1 	evice : evice type CPU 1512C-1 PN  evice evice s of 4 accessible devices for	Interfa PN/IE PN/IE	Ice type Add 10. Acc	Show all compatible dress 1.29.191 cess address	e devices Target device PLC_1  Start search message
e status informatio an completed. 1 an and informatio	Select target de Device PLC_1  device Accessible d compatible device ion retrieval comple	evice: Device type CPU 1512C-1 PN  evice evice s of 4 accessible devices for eted.	Interfa PN/IE PN/IE	ice type Add 10. Acc	Show all compatible dress 1.29.191 cess address Display only error	e devices Target device PLC_1  <u>Start search</u> messager

# Found the targeted PLC and select it

# Load the config and code to the PLC

Status	1	Target	Message	Action
<b>↓</b>	0	▼ PLC_1	Ready for loading.	Load 'PLC_1'
	0	Software	Download software to device	Consistent download
	0	Text libraries	Download all alarm texts and text list texts to device	Consistent download
<			WI	>
<			III	Refresh

# Check the loading option

# Load the config and code to the PLC

#### If the download success, make the TIA portal online with the PLC

Name	Data type	Default value	Comment	- Op - Put	en the : the IP	GIC ad	LS IG	este ss "	er 10.	1.2	9.1	L94'	,		tus 14 115 1 014 015 0	
🕣 🔹 Initial_Call	Bool		Initial call of this OB	- Ch	ack if t	ho	stati	uc i	a hl		or	not				
🖘 🔹 Remanence	Bool		=True, if remanent data are available	- Chi			stati	US I:		iue	01	1101				
Network 1:				GICS Test	er									\		
Comment									_							
				Target IP	10.1.29.1	94			Po	ort	2015	j				
%12.0 "Input 1"			%Q6.0 "Output 1"													
			{ }	AO1	512				A	11	2060	)				
				402	512				۵	12	2048	_				
%12.1			%Q6.1	402	510	-			A	12	1726					
"Input 2"			"Output 2"	A03	512	-			~	13	1720			<b>St</b> -1		
				A04	512	-			A	14	1725			Stat	JS	
%12.2			%O6.2	AOS	512	- 11			A	15	2050	)				
"Input 3"			"Output 3"	AO6	512	_			A	16	2055	i				
			{ }	AO7	512				A	17	2051	1				
				AO8	512				A	18	2047	1				
%12.3 "Input 4"			%Q6.3 "Output 4"													
				101 102	103 104	105	106	107	08	109	110	111	112 I	13 I1	4 115	1
				O01 O02	2 003 004	005	O06	007	<b>008</b>	O09	010	011	012 0	013 C	14 01	5 01



If the status bar in the GICS Tester is not blue,








## There are light indicators of I/O on the card

## Exercise

- Create a project based on the mixing problem shown on the right figure
- Map I/O to the emulation card and emulate the system

